



282	284	286	288	290
Object Address	Object name	Number of bytes	Memory type	Hardware address
01	Control word init	1	EEPROM	0x00
02	Status word	1	RAM	0x00
03	Setpoint speed	2	EEPROM	0x01
04	Actual speed	2	RAM	0x01
...
...
32	Manufacturer	1	ROM	0x00
33	SW version	1	ROM	0x01
...

Fig. 19

Object Address	Object name	Number of bytes	Memory type	Hardware address
280	282	286	288	290
AA	BB	CC	DD	EE
01	B1	1	EEPROM	0x00
02	B2	1	RAM	0x00
03	B3	2	EEPROM	0x01
04	B4	2	RAM	0x01
...
...
32	B32	1	ROM	0x00
33	B33	1	ROM	0x01
...

Control word init
 Status word
 Setpoint speed
 Actual speed
 Manufacturer
 SW version

Fig. 19

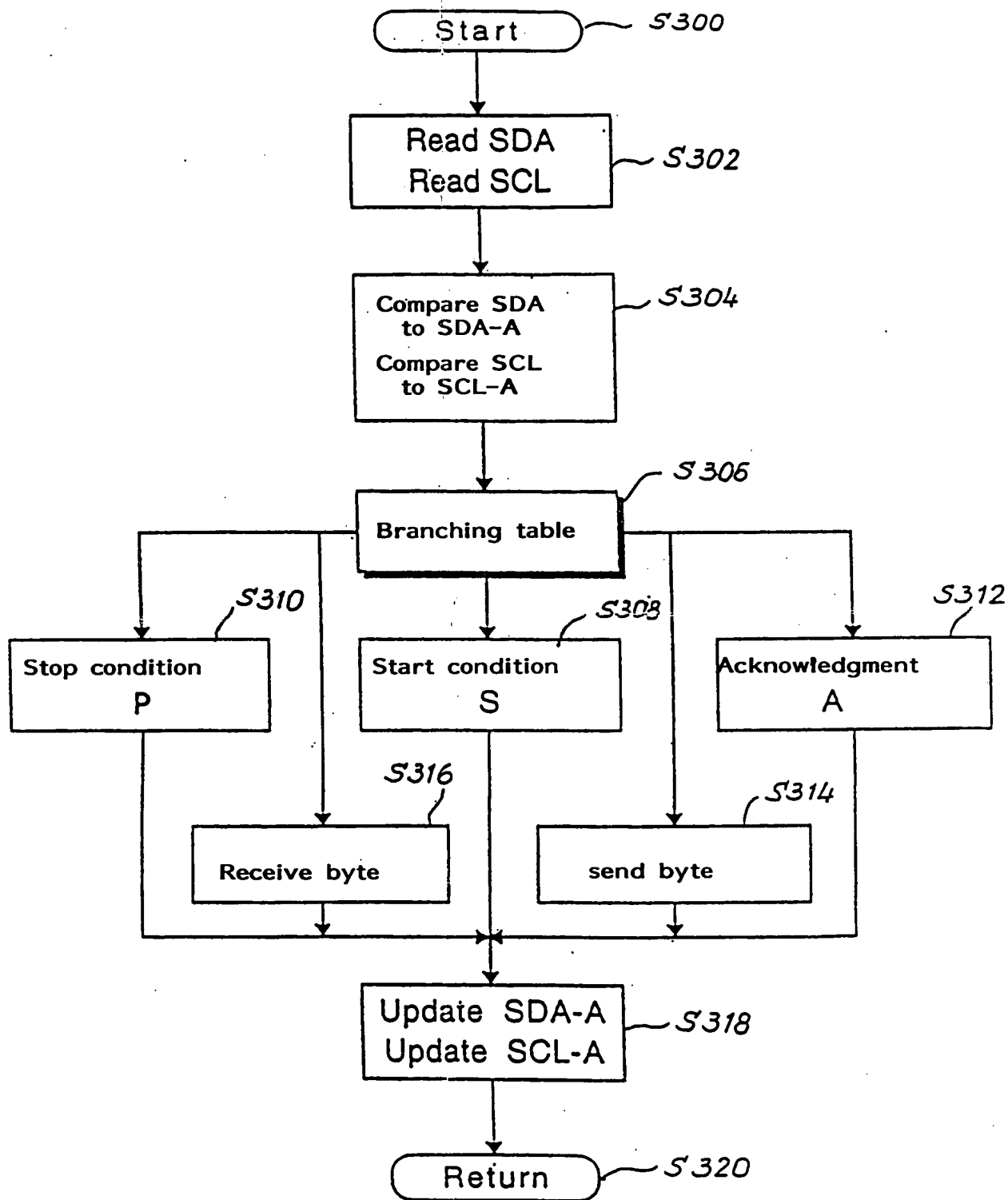


Fig. 21

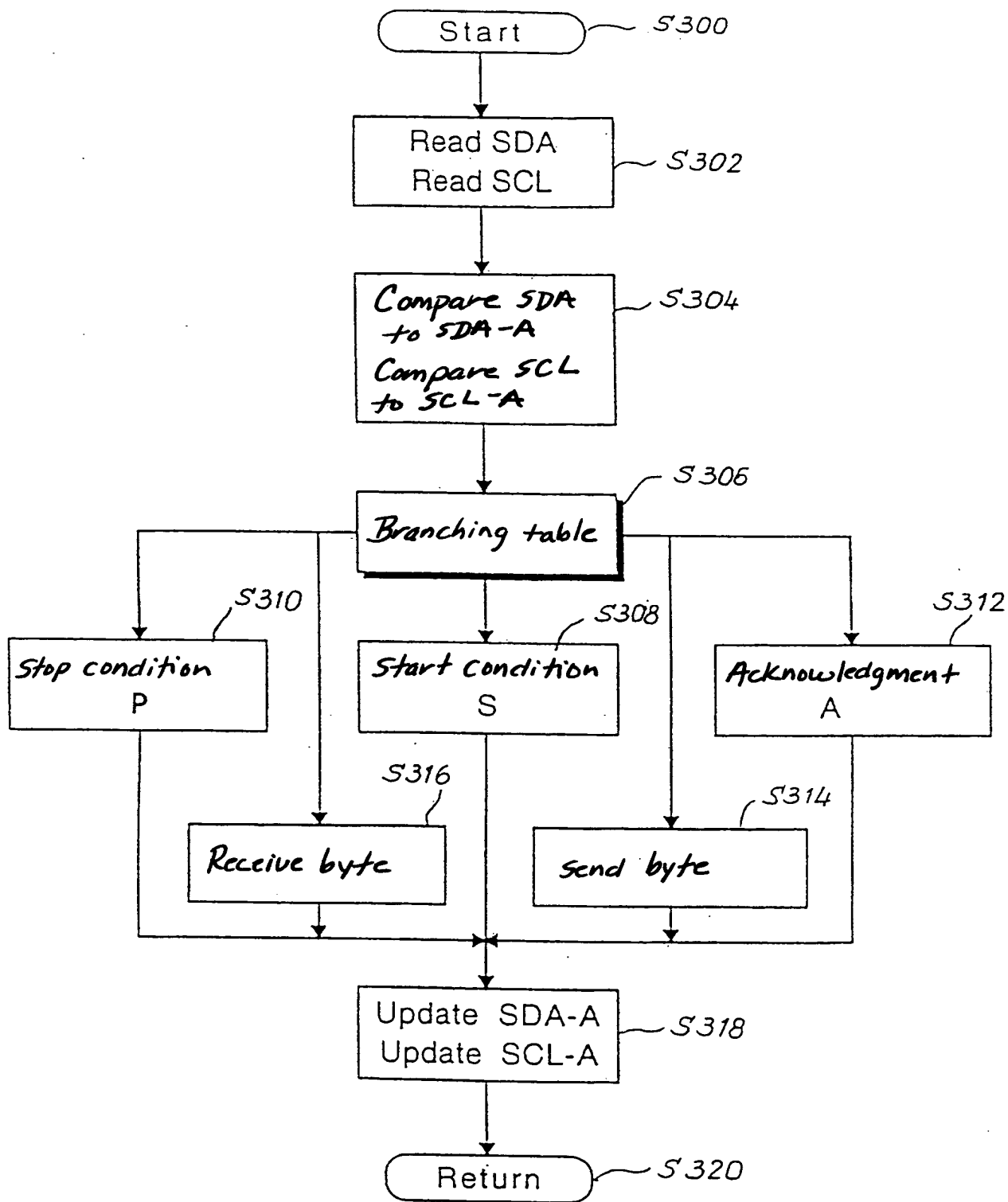


Fig. 21